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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/607,005	06/27/2003	Koji Masuda	239545US2	4936	
22850 75	590 . 12/28/2004		EXAMINER		
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			PHAM, HAI CHI		
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ALEXANDRIA	A, VA 22314		ART UNIT PAPER NUMBER		
			2861		
				DATE MAILED: 12/28/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
	10/607,005	MASUDA, KOJI				
Office Action Summary	Examiner	Art Unit	-			
	Hai C Pham	2861				
The MAILING DATE of this communication Period for Reply A SHORTENED STATUTORY PERIOD FOR RE	•					
THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory per - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the meanned patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may a reply within the statutory minimum of the riod will apply and will expire SIX (6) MC atute, cause the application to become A	reply be timely filed irty (30) days will be considered timely. ONTHS from the mailing date of this communications (35 U.S.C. § 133).	cation.			
Status						
1) Responsive to communication(s) filed on <u>0</u> 8	8 October 2004.					
2a) ☐ This action is FINAL . 2b) ☒ T	This action is non-final.					
3) Since this application is in condition for allo						
closed in accordance with the practice unde	er <i>Ex parte Quayle</i> , 1935 C.	D. 11, 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-5 and 7-22 is/are pending in the	application.					
4a) Of the above claim(s) is/are with	drawn from consideration.	•				
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-5,7-13 and 16-22</u> is/are rejected	l .					
7)⊠ Claim(s) <u>14,15 and 20</u> is/are objected to.						
8) Claim(s) are subject to restriction an	d/or election requirement.					
Application Papers						
9)☐ The specification is objected to by the Exam	niner.					
10) The drawing(s) filed on 27 June 2003 is/are	: a) ☐ accepted or b) ☒ obj	ected to by the Examiner.				
Applicant may not request that any objection to	the drawing(s) be held in abey	ance. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the cor	rection is required if the drawin	g(s) is objected to. See 37 CFR 1.1	21(d).			
11) The oath or declaration is objected to by the	Examiner. Note the attach	ed Office Action or form PTO-15	2.			
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docum 		§ 119(a)-(d) or (f).				
2. Certified copies of the priority docum		Application No				
3. Copies of the certified copies of the papplication from the International But	oriority documents have bee		9			
* See the attached detailed Office action for a		ot received.				
· · · · · · · · · · · · · · · · · · ·						
Attachment(s)						
1) Notice of References Cited (PTO-892)	· —	/ Summary (PTO-413) o(s)/Mail Date				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB	·	f Informal Patent Application (PTO-152)				
Paper No(s)/Mail Date	6) Other:	·				

DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference signs mentioned in the description: "LK" (for pitch of the image forming devices). Figures 30 through 33. show the pitch of the image forming devices as being "LP" Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claim 7 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 7 recites the following limitation "wherein **the light volume** of the light emitting devices that are located on and near the edge of the light emitting device array chips **is set up** <u>such that</u> an interval Pa between one of the light emitting devices on the edge of one of the light emitting device array chips and another one of the light emitting devices on the edge of an adjacent one of the light emitting device array chips is different from the predetermined interval P and <u>such that</u> Pa>1.1P or Pa<0.9P" (emphasis added), which is not clearly understood in that how the set up of the light volume would <u>cause</u> and/or <u>affect</u> the interval Pa. The other way around seems more plausible.

Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1-5, 8-13 and 21-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Obata (U.S. 6,172,700) in view of Yamanaka et al. (JP 2001-138568).

Obata discloses a writing device for an image forming apparatus a light emitting device array (LED array 1) that further comprises a plurality of light emitting device array chips (LED chips 3), each of which comprises a plurality of light emitting devices (LEDs

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2) that are arranged at a predetermined interval P (distance a between nearby LEDs), and an image forming device array that further comprises image forming devices (although not shown, the imaging lenses such as the rod lens array are inherently included in the electrostatic printing device using a light emitting device array for forming an electrostatic latent image on a photosensitive drum), wherein the light volume of the light emitting devices that are located on and near an edge of the light emitting device array chip can be set differently from other light emitting devices (due to gap between nearby LED chips 3, which is different from the distance a between nearby LEDs within each LED chip, the quantity of light emitted by the LEDs located at the ends of each of the LED chips). With regard to claims 11 and 21, Obata further teaches the light volume of the light emitting devices being set up based on the gradient of an approximated regression line for exposure areas corresponding to a plurality of the light emitting devices (the corrected image data corresponding to the interval between nearby LED chips being of halftone).

However, Obata fails to explicitly teach the light volume of the light emitting devices being set up such that a predefined property value concerning an exposure intensity distribution of each of the light emitting devices falls within a predetermined range, the predetermined range being defined for an effective image area in its entirety, the determination of the correlation between the light volume and the property value of the light emitting device, and the compensation value being a driving current, more than half elements being located at ends of the LED chip, and the predetermined cycle.

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Yamanaka et al. discloses a method of compensating for output variations in a printing head, which comprises a plurality of LED chips (2), each having a plurality of light emitting elements, wherein the light intensity distribution for the plural light emitting elements is acquired (Fig. 4) by measuring the intensity of light emitted by the light emitting elements at a predetermined cycle to determine a property value of the intensity distribution, which lies between the IMAX and IMIN values in order to adjust the light intensity emitted by each of the light emitting elements, based on which the driving current is adjusted for each of the light emitting elements to obtain a uniform illuminance across the image exposure area.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to set the compensation of the light intensity distribution of the LED units of Obata by correcting the corresponding driving currents such that the light intensity distribution is uniform as taught by Yamanaka et al. The motivation for doing so would have been to correct the inherent unevenness illuminance of the individual light emitting elements to produce a highly uniform light distribution across the image exposure area.

The method claims 10 and 22 are deemed to be clearly anticipated by the functions of the above structures.

6. Claims 7 and 16-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Obata in view of Yamanaka et al., as applied to claim 11 above, and further in view of Sawada (JP 8-118722).

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Obata, as modified by Yamanaka et al., discloses all the basic limitations of the claimed invention except for the light volume of each of the light emitting devices is set up based on the magnitude of the interval of the light emitting device located at the ends of the LED chips as compared to the interval between nearby LEDs.

Sawada, an acknowledged prior art, discloses a driving circuit for an LED print head, which comprises a plurality of LED array chips having a gap G between adjacent LED array chips being different from the interval P of the light emitting elements, wherein the driving current flowing though the light emitting elements located at the ends of each LED array chip varies dependent on the difference between the intervals G and P, namely the driving current is increased when G is larger than P and is decreased when G is smaller than P (see English Translation, paragraphs [0033] and [0034]).

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to adjust the driving current supplied to the light emitting elements disposed at the end of the LED array chip in accordance to the relationship between the intervals G and P as taught by Sawada in the modified device of Obata. The motivation for doing so would have been doing to be able to effectively cancel the density difference occurred at the gap between two adjoining LED array chips, which is dependent on the discrepancy between the above gap and the pitch of the light emitting elements as explained by Sawada.

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Allowable Subject Matter

7. Claims 14-15 and 20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Pertinent Prior Art

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Ishii (JP 2001-080111) discloses an image forming apparatus comprising a print head having a plurality of light emitting device array chips, wherein the interval between one of the light emitting devices on the edge of one of the light emitting device array chips and another one of the light emitting devices on the edge of an adjacent one of the light emitting device array chips is set at Pa = [66 μ m – 69 μ m] as compared to the predetermined interval between adjacent light emitting points being set at P = 63.5 μ m or

$$1.03 P \le Pa \le 1.08 P$$

Response to Arguments

9. Applicant's arguments with respect to claims 1-22 have been considered but are most in view of the new grounds of rejection presented in this Office action.

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Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hai C Pham whose telephone number is (571) 272-2260. The examiner can normally be reached on M-F 8:30AM - 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L Talbott can be reached on (571) 272-1934. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

HAI PHAM
PRIMARY EXAMINER

Harzliffiam -

December 21, 2004